



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sommer Docket No.: INF-126
Serial No.: 10/753,407 Art Unit: 2822
Filed: January 9, 2004 Examiner: Ida M. Soward
Title: Integrated Semiconductor Memory Circuit and Method of Manufacturing the Same

Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's
Office Action mailed May 19, 2005. Please amend the above-referenced application as follows.